

PATENT APPLICATION
Docket No. 2522-026
Client No. AW8116US/GJ

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Ji-Young Kim and Hyoung-Sub Kim

Serial No. Not yet assigned Examiner: Not yet assigned

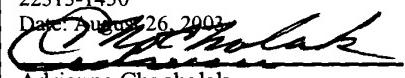
Filed: August 25, 2003 Group Art Unit: Not yet Assigned

For: INTEGRATION METHOD OF A SEMICONDUCTOR DEVICE
HAVING A RECESSED GATE ELECTRODE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to Examination, please amend the application as follows.

I hereby certify that this correspondence
is being deposited with the United States
Postal Service as Express mail in an
envelope addressed to: Commissioner for
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Date: August 26, 2003

Adrienne Chocholak